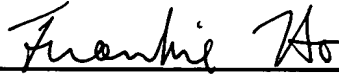


PATENT  
Docket No. INTEL/17877

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicant: Iyer	)	I hereby certify that this paper is
Serial No.: 10/722,884	)	being deposited with the United
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REQUESTS BASED ON PRIORITY	)	Box 1450, Alexandria, VA 22313-
Group Art Unit: Not Yet Assigned	)	1450 on this date:
Examiner: Not Yet Assigned	)	<b>December 15, 2003</b>
	)	
	)	Frankie Ho
	)	Registration No. 48,479
	)	Attorney for Applicant

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

### **TIME OF FILING**

This information disclosure statement is being filed with within three months of the filing date of the application. In accordance with 37 CFR §1.97(b), no certification or fee is required.

### **METHOD OF PAYMENT**

☒ No fee is required.

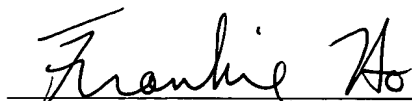
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

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**December 15, 2003**

**Attorneys for Intel Corporation**

Form PTO-1449 (Modified)

U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.

Serial No.

INTEL/17877

10/722,884

Applicant

Iyer

Filing Date

November 26,  
2003

Group Art Unit

Not Yet Assigned

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01					

## FOREIGN PATENT DOCUMENTS

*Examiner Initials	Document Number	Publication Date	Country	Class	Subclass	Translation Yes                  No
	B01					

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	Chiou et al., <i>Application-Specific Memory Management for Embedded Systems Using Software-Controlled Caches</i> , Proceedings of the 37 <sup>th</sup> Conference on Design Automation, pp. 416-419 (2000).
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EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.